

710	240
Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

U.S. UTILITY Patent Application

Q.I.P.E. <i>R.H.</i> (2) SCANNED <i>SB</i> Q.A. <i>W</i>	PATENT DATE .
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APPLICATION NO. 09/637846	CONT/PRIOR	CLASS 700-710	SUBCLASS 240	ART UNIT 2121 2409	EXAMINER HUYANH
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APPLICANTS

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TITLE

Peripheral component interconnect arbiter implementation with dynamic priority scheme

PTO-2040
12/89

ISSUING CLASSIFICATION									
ORIGINAL				CROSS REFERENCE(S)					
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
710		240		710	244				
INTERNATIONAL CLASSIFICATION									
G	O	I	F	13138					

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg. <div>5</div>	Figs. Drwg. <div>5</div>	Print Fig. <div>3</div>	Total Claims <div>9</div>	Print Claim for O.G. <div>1</div>
The term of this patent subsequent to _____ (date) has been disclaimed.	<div>Kim T. Huynh</div> (Assistant Examiner)			<div>11/15/03</div> (Date)	
The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	<div>BEST AVAILABLE COPY</div>			NOTICE OF ALLOWANCE MAILED	
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The terminal ____ months of this patent have been disclaimed.	<div>_____</div> (Primary Examiner)			Amount Due	Date Paid
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